



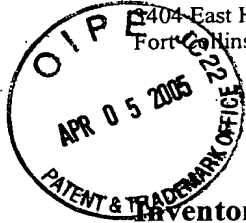
Attorney Docket No: 200315915-1

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PATENT APPLICATION

Attorney Docket No: 200315915-1



IN THE
UNITED STATES PATENT AND TRADEMARK OFFICE

Inventors: Hai Q. Chiang

Examiner: Joseph Nguyen

Application No: 10/799,471

Group Art Unit: 2815

Filing Date: Mar. 12, 2004

Confirmation No: 7711

Title: SEMICONDUCTOR DEVICE

COMMISSIONER FOR PATENTS
PO Box 1450
Alexandria, VA 22313-1450

RESPONSE TO RESTRICTION REQUIREMENT

Dear Sir:

In response to Examiner's Office Communication dated March 3, 2005 Applicants respond as follows:


Examiner has identified two distinct inventions:

- I. Claims 1-13, 33-39, drawn to a semiconductor device, classified in class 257, subclass 368.
- II. Claims 14-32 drawn to a method of making a semiconductor substrate, classified in class 438, subclass 1+.

Applicants hereby elect Group I, Claims 1-13, and 33-39 in class 257, subclass 368 without traverse. Applicants would also like to respectfully point out to Examiner that claims 14-32 are drawn to a method of making a semiconductor device.

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Respectfully submitted,
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